

Application for Patent
for

METHOD, SYSTEM, AND PROGRAM FOR MANAGING
DATA TRANSMISSION THROUGH A NETWORK

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BACKGROUND OF THE INVENTION

5 1. Field of the Invention

[0001] The present invention relates to a method, system, and program for managing data transmission through a network.

2. Description of Related Art

10 [0002] In a network environment, a network adaptor on a host computer, such as an Ethernet controller, Fibre Channel controller, etc., will receive Input/Output (I/O) requests or responses to I/O requests initiated from the host. Often, the host computer operating system includes a device driver to communicate with the network adaptor hardware to manage I/O requests to transmit over a network. The host computer further includes a transport protocol
15 driver which packages data to be transmitted over the network into packets, each of which contains a destination address as well as a portion of the data to be transmitted. Data packets received at the network adaptor are often stored in an available allocated packet buffer in the host memory. The transport protocol driver processes the packets received by the network adaptor that are stored in the packet buffer, and accesses any I/O commands or data
20 embedded in the packet.

[0003] For instance, the transport protocol driver may implement the Transmission Control Protocol (TCP) and Internet Protocol (IP) to encode and address data for transmission, and to decode and access the payload data in the TCP/IP packets received at the network adaptor. IP specifies the format of packets, also called datagrams, and the addressing scheme. TCP is a
25 higher level protocol which establishes a connection between a destination and a source. A still higher level protocol, Remote Direct Memory Access (RDMA) establishes a higher level

connection and permits, among other operations, direct placement of data at a specified memory location at the destination.

[0004] A device driver can utilize significant host processor resources to handle network transmission requests to the network adaptor. One technique to reduce the load on the host processor is the use of a TCP/IP Offload Engine (TOE) in which TCP/IP protocol related operations are implemented in the network adaptor hardware as opposed to the device driver, thereby saving the host processor from having to perform some or all of the TCP/IP protocol related operations. The transport protocol operations include packaging data in a TCP/IP packet with a checksum and other information, and unpacking a TCP/IP packet received from over the network to access the payload or data.

[0005] FIG. 1 illustrates a stream 10 of TCP/IP packets which are being sent from a source host to a destination host in a TCP connection. In the TCP protocol as specified in the industry accepted TCP RFC (request for comment), each packet is assigned a unique sequence number. As each packet is successfully sent to the destination host, an acknowledgment is sent by the destination host to the source host, notifying the source host by packet sequence number of the successful receipt of that packet. Accordingly, the stream 10 includes a portion 12 of packets which have been both sent and acknowledged as received by the destination host. The stream 10 further includes a portion 14 of packets which have been sent by the source host but have not yet been acknowledged as received by the destination host. The source host maintains a TCP Unacknowledged Data Pointer 16 which points to the sequence number of the first unacknowledged sent packet. The TCP Unacknowledged Data Pointer 16 is stored in a field 17a, 17b ... 17n (FIG. 2) of a Protocol Control Block 18a, 18b ... 18n, each of which is used to initiate and maintain one of a plurality of associated TCP connections between the source host and one or more destination hosts.

[0006] The capacity of the packet buffer used to store data packets received at the destination host is generally limited in size. In accordance with the TCP protocol, the destination host advertises how much buffer space it has available by sending a value referred

to herein as a TCP Window indicated at 20 in FIG. 1. Accordingly, the source host uses the TCP Window value to limit the number of outstanding packets sent to the destination host, that is, the number of sent packets for which the source host has not yet received an acknowledgment. The TCP Window value for each TCP connection is stored in a field 21a, 21b ... 21n of the Protocol Control Block 18a, 18b ... 18n which controls the associated TCP connection.

5 [0007] For example, if the destination host sends a TCP Window value of 128 KB (kilobytes) for a particular TCP connection, the source host will according to the TCP protocol, limit the amount of data it sends over that TCP connection to 128 KB until it receives an acknowledgment from the destination host that it has received some or all of the data. If the destination host acknowledges that it has received the entire 128 KB, the source host can send another 128 KB. On the other hand, if the destination host acknowledges receiving only 96 KB, for example, the host source will send only an additional 96 KB over that TCP connection until it receives further acknowledgments.

15 [0008] A TCP Next Data Pointer 22 stored in a field 23a, 23b ... 23n of the associated Protocol Control Block 18a, 18b ... 18n, points to the sequence number of the next packet to be sent to the destination host. A portion 24 of the datastream 10 between the TCP Next Data Pointer 22 and the end 28 of the TCP Window 20 represents packets which have not yet been sent but are permitted to be sent under the TCP protocol without waiting for any additional acknowledgments because these packets are still within the TCP Window 20 as shown in FIG. 1. A portion 26 of the datastream 10 which is outside the end boundary 28 of the TCP Window 20, is not permitted to be sent under the TCP protocol until additional acknowledgments are received.

[0009] As the destination host sends acknowledgments to the source host, the TCP Unacknowledged Data Pointer 16 moves to indicate the acknowledgment of additional packets for that connection. The beginning boundary 30 of the TCP Window 20 shifts with the TCP

Unacknowledged Data Pointer 16 so that the TCP Window end boundary 28 also shifts so that additional packets may be sent for the connection.

[0010] FIG. 3 illustrates a plurality of RDMA connections 50a, 50b ... 50n, between various software applications of a source host and various storage locations of one or more destination
5 hosts through a network. Each RDMA connection 50a, 50b ... 50n runs over a TCP connection. In the RDMA protocol, as defined in the industry accepted RDMA RFC (request for comment), each RDMA connection 50a, 50b ... 50n includes a queue pair 51a, 51b.... 51n comprising a queue 52a, 52b ... 52n which is created by a software application which intends to send messages to be stored at a specified memory location of a destination host. Each
10 application queue 52a, 52b ... 52n stores the messages to be sent by the associated software application. The size of each queue 52a, 52b ... 52n may be quite large or relatively small, depending upon the number of messages to be sent by the associated application.

[0011] Each queue pair 51a, 51b ... 51n of each RDMA connection 50a, 50b ... 50n further includes a network interface queue 60a, 60b ... 60n which is paired with the associated
15 application queue 52a, 52b ... 52n of the software applications. The network interface 62 includes various hardware, typically a network interface card, and various software including drivers which are executed by the host. The network interface may also include various offload engines to perform protocol operations.

[0012] In response to a request from an application to send messages to be stored at a
20 specified destination host memory address at the other end of one of the RDMA connections 50a, 50b ... 50n, a network interface 62 obtains a message credit designated an "empty message" from a common pool 64 of empty messages. The size of the pool 64, that is, the number of messages which the network interface 62 can handle, is typically a function of the hardware capabilities of the network interface 62. If an empty message is available from the
25 pool 64, a message is taken from the application queue 52a, 52b ... 52n of the requesting application and queued in the corresponding network interface queue 60a, 60b 60n of the queue pair 51a, 51b ... 51n of the particular RDMA connection 50a, 50b ... 50n. The

messages queued in the network interface queues 60a, 60b ... 60n are sent over the network to the specified memory addresses of the destination hosts which acknowledge each message which is successfully received and stored at the specified memory address. Messages sent but not yet acknowledged are referred to herein as “uncompleted sent messages.” Once a message
5 is acknowledged as successfully received and stored by the destination host, an empty message is restored or replenished in the pool 64 of empty messages.

[0013] In accordance with the RDMA protocol, the total number of messages queued in all the network interface queues 60a, 60b ... 60n plus the total number of uncompleted messages sent by all the RDMA connections 50a, 50b ... 50n typically is not permitted to exceed the size
10 of the pool 64 of empty messages. Once one of the RDMA connections 50a, 50b ... 50n reaches the limit imposed by the pool 64 of empty messages, no more RDMA messages from any of the connections 50a, 50b ... 50n may be queued and sent until additional acknowledgments are received.

[0014] Notwithstanding, there is a continued need in the art to improve the performance of
15 connections.

BRIEF DESCRIPTION OF THE DRAWINGS

[0015] Referring now to the drawings in which like reference numbers represent corresponding parts throughout:

20 FIG. 1 illustrates a stream of data being transmitted in accordance with the prior art TCP protocol;

FIG. 2 illustrates prior art Protocol Control Blocks in accordance with the TCP protocol;

FIG. 3 illustrates a prior art network interface and message queues of RDMA
25 connections;

FIG. 4 illustrates one embodiment of a computing environment in which aspects of the invention are implemented;

FIG. 5 illustrates a prior art packet architecture;

FIG. 6 illustrates one embodiment of a stream of data being transmitted in accordance with aspects of invention;

FIG. 7 illustrates one embodiment of operations performed to manage a transmission of
5 data in accordance with aspects of the invention;

FIG. 8 illustrates one embodiment of a data structure to store information used to manage transmission of data in accordance with aspects of the invention;

FIG. 9 illustrates one mode of the operations of FIG. 7;

FIG. 10 illustrates one embodiment of a network interface and message queues for
10 RDMA connections in accordance with aspects of the invention;

FIG. 11 illustrates another embodiment of operations performed to manage a transmission of data in accordance with aspects of the invention; and

FIG. 12 illustrates an architecture that may be used with the described embodiments.

15 DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0016] In the following description, reference is made to the accompanying drawings which form a part hereof and which illustrate several embodiments of the present invention. It is understood that other embodiments may be utilized and structural and operational changes may be made without departing from the scope of the present invention.

20 [0017] FIG. 4 illustrates a computing environment in which aspects of the invention may be implemented. A computer 102 includes one or more a central processing units (CPU)104 (only one is shown), a volatile memory 106 , non-volatile storage108 , an operating system 110, and a network adaptor 112. An application program 114 further executes in memory 106 and is capable of transmitting and receiving packets from a remote computer. The
25 computer 102 may comprise any computing device known in the art, such as a mainframe, server, personal computer, workstation, laptop, handheld computer, telephony device, network appliance, virtualization device, storage controller, etc. Any CPU 104 and operating system

110 known in the art may be used. Programs and data in memory 106 may be swapped into storage 108 as part of memory management operations.

[0018] The network adaptor 112 includes a network protocol layer 116 for implementing the physical communication layer to send and receive network packets to and from remote
5 devices over a network 118. The network 118 may comprise a Local Area Network (LAN), the Internet, a Wide Area Network (WAN), Storage Area Network (SAN), etc. The embodiments may be configured to transmit data over a wireless network or connection, such as wireless LAN, Bluetooth, etc. In certain embodiments, the network adaptor 112 and network protocol layer 116 may implement the Ethernet protocol, token ring protocol, Fibre
10 Channel protocol, Infiniband, Serial Advanced Technology Attachment (SATA), parallel SCSI, serial attached SCSI cable, etc., or any other network communication protocol known in the art.

[0019] A device driver 120 executes in memory 106 and includes network adaptor 112 specific commands to communicate with the network adaptor 112 and interface between the
15 operating system 110 and the network adaptor 112. In certain implementations, the network adaptor 112 includes a transport protocol layer 121 as well as the network protocol layer 116. For example, the network adaptor 112 can implement a TCP/IP offload engine (TOE), in which transport layer operations are performed within the offload engines of the transport protocol layer 121 implemented within the network adaptor 112 hardware, as opposed to the
20 device driver 120.

[0020] The network layer 116 handles network communication and provides received TCP/IP packets to the transport protocol layer 121 to decrypt the packets if encrypted. The transport protocol layer 121 interfaces with the device driver 120 and performs additional transport protocol layer operations, such as processing the decrypted content of messages
25 included in the packets received at the network adaptor 112 that are wrapped in a transport layer, such as TCP and/or IP, the Internet Small Computer System Interface (iSCSI), Fibre Channel SCSI, parallel SCSI transport, or any other transport layer protocol known in the art.

The transport offload engine 121 can unpack the payload from the received TCP/IP packet and transfer the data to the device driver 120 to return to the application 114.

[0021] In certain implementations, the network adaptor 112 can further include an RDMA protocol layer 122 as well as the transport protocol layer 121. For example, the network
5 adaptor 112 can implement an RDMA offload engine, in which RDMA layer operations are performed within the offload engines of the RDMA protocol layer 122 implemented within the network adaptor 112 hardware, as opposed to the device driver 120.

[0022] Thus, an application 114 transmitting messages over an RDMA connection can transmit the message through the device driver 120 and the RDMA protocol layer 122 of the
10 network adaptor 112. The data of the message can be sent to the transport protocol layer 121 to be packaged in a TCP/IP packet. The transport protocol layer 121 can further encrypt the packet before transmitting it over the network 118 through the network protocol layer 116.

[0023] The memory 106 further includes file objects 124, which also may be referred to as socket objects, which include information on a connection to a remote computer over the
15 network 118. The application 114 uses the information in the file object 124 to identify the connection. The application 114 would use the file object 124 to communicate with a remote system. The file object 124 may indicate the local port or socket that will be used to communicate with a remote system, a local network (IP) address of the computer 102 in which the application 114 executes, how much data has been sent and received by the application
20 114, and the remote port and network address, e.g., IP address, with which the application 114 communicates. Context information 126 comprises a data structure including information the device driver 120 maintains to manage requests sent to the network adaptor 112 as described below.

[0024] FIG. 5 illustrates a format of a network packet 150 received at or transmitted by the
25 network adaptor 112. An RDMA message may include one or many such packets 150. The network packet 150 is implemented in a format understood by the network protocol 114, such as encapsulated in an Ethernet frame that would include additional Ethernet components, such

as a header and error checking code (not shown). A transport packet 152 is included in the network packet 150. The transport packet may 152 comprise a transport layer capable of being processed by the transport protocol driver 121, such as the TCP and/or IP protocol, Internet Small Computer System Interface (iSCSI) protocol, Fibre Channel SCSI, parallel
5 SCSI transport, etc. The transport packet 152 includes payload data 154 as well as other transport layer fields, such as a header and an error checking code. The payload data 154 includes the underlying content being transmitted, e.g., commands, status and/or data. The operating system 110 may include a device layer, such as a SCSI driver (not shown), to process the content of the payload data 154 and access any status, commands and/or data
10 therein.

[0025] If a particular TCP connection of the source host is accorded a relatively large TCP window 20 (FIG. 1) when sending data over the TCP connection to a destination host, it is appreciated that the TCP connection having the large TCP window can continue sending data in a manner which uses up the resources of the source host to the exclusion of other TCP
15 connections of the source host. As a consequence, the other TCP connections of the source host may be hindered from sending data. In one implementation as shown in FIG. 6, the computer 102 when sending data over a TCP connection imposes a Virtual Window 200 which can be substantially smaller than the TCP Window provided by the destination host of the TCP connection. When the TCP Next Data Pointer 22 reaches the end boundary 202 of
20 the Virtual Window 200, the host source 102 stops sending data over that TCP connection even though the TCP Next Data Pointer 22 has not yet reached the end boundary 28 of the TCP Window 20. As a consequence, other connections are provided the opportunity to utilize the resources of the computer 102 such that the resources may be shared more fairly.

[0026] FIG. 7 illustrates operations of the device driver 120 and the network adapter 122 in
25 using a Virtual Window 200 to distribute the data transmission resources of the computer 102. In response to a request by a software application 114, a TCP connection is established (block 210) between the computer 102 and a destination host. In establishing the TCP connection, a

Protocol Control Block such as one of the Protocol Control Blocks 222a, 222b ... 222n (FIG. 8) is populated in a manner similar to the Protocol Control Blocks 18a, 18b ... 18n of FIG. 2. Each Protocol Control Block 222a, 222b ... 222n has a field 17a, 17b ... 17n for storing the TCP Unacknowledged Data Pointer 16, a field 21a, 21b ... 21n for storing the TCP Window, and a field 23a, 23b ... 23n for storing a TCP Next Pointer of the associated TCP connection in accordance with the TCP RFC.

[0027] In this implementation, the Virtual Window 200 for this TCP connection has a maximum value referred herein as a Virtual Window Maximum which is stored in a field 224a, 224b ... 224n of the associated Protocol Control Block 222a, 222b ... 222n. The size of the TCP Window 20 received from the destination host of the TCP connection is compared (block 230) to the size of the Virtual Window Maximum. If the TCP Window 20 is not smaller than the Virtual Window Maximum, the size of the Virtual Window 200 is set (block 232) to the size of the Virtual Window Maximum stored in the field 224a, 224b ... 224n of the Protocol Control Block 222a, 222b ... 222n which controls the TCP connection. The size of the Virtual Window 200 is stored in the field 233a, 233b ... 233n of the Protocol Control Block 222a, 222b ... 222n which controls the TCP connection.

[0028] Prior to sending any data, the TCP Unacknowledged Data Pointer 16 of the Protocol Control Block 222a, 222b ... 222n which controls the TCP connection is set to the sequence number of the first data packet to be sent. The computer 102 initiates (block 234) the sending of packets of data to the destination host. The amount of data sent in this step may vary depending upon the particular application. However, in many applications the amount of data sent in this step will be a relatively small proportion of the size of the Virtual Window 200. The TCP Next Data Pointer 22 of the Protocol Control Block 222a, 222b ... 222n which controls the TCP connection is set to the sequence number of the next data packet to be sent.

[0029] Following the sending of these data packets, a check (block 236) is made to determine if the destination host has acknowledged receiving any of the sent data packets. If so, the Virtual Window 200 (FIG. 6) is moved (block 240) by moving forward the TCP

Unacknowledged Data Pointer 16 of the Protocol Control Block 222a, 222b ... 222n which controls the TCP connection. The TCP Unacknowledged Data Pointer 16 is moved forward in the sequence to mark the beginning of the sequence numbers of the data packets which have been sent but not yet acknowledged. Those data packets which have been sent but not yet
5 acknowledged are indicated in FIG. 6 as a portion 14 of the data stream 250 of data packets being sent. Those data packets which have both been sent and acknowledged are indicated in FIG. 6 as portion 12. The TCP Unacknowledged Data Pointer 16 marks the boundary 252 between the sent and acknowledged packets 12 and the sent but not acknowledged packets 14 of the stream 250. The TCP Unacknowledged Data Pointer 16 also marks the start
10 boundary 254 of the Virtual Window 200 and the TCP Window 20.

[0030] Hence as the TCP Unacknowledged Data Pointer 16 is moved (block 240), the Virtual Window 200 and the TCP Window 20 are moved as well when the destination host acknowledges (block 236) the receipt of data packets through this particular TCP connection with the computer 110. Alternatively, if no packets have been acknowledged (block 236), the
15 Virtual Window 200, the TCP Window 20 and the TCP Unacknowledged Data Pointer 16 remain unmoved.

[0031] Those data packets which have not yet been sent but are permitted to be sent without receipt of any further acknowledgments are indicated in FIG. 6 as a portion 256. The TCP Next Data Pointer 22 marks the start boundary 258 of the portion 256 and the end boundary
20 202 of the Virtual Window 200 marks the end boundary 260 of the permitted but not yet sent datastream portion 256. If the TCP Next Data Pointer 22 has reached the end boundary 202 of the Virtual Window 200, indicating that the stream of sent data has reached (block 262) the end of the Virtual Window 200, the portion 256 is of zero size and no further data packets can be sent until additional acknowledgments are received (block 236) from the destination host.
25 Once additional acknowledgments are received, the Virtual Window 200 (block 240) will be moved thereby forming a new portion 256 and permitting the sending of additional packets.

- [0032] It is noted that the stream of sent but unacknowledged data packets (portion 14) is paused when it reaches the end boundary 202 of the Virtual Window 200 rather than the end boundary 28 of the larger TCP Window 20. Thus a portion 264 of the datastream 250 between the end boundary 202 of the Virtual Window 200 and the end boundary 28 of the TCP Window 20 which would have been permitted to be sent without waiting for additional acknowledgments is withheld from being sent until additional acknowledgments are received. As a consequence, other connections may utilize the resources of the computer 110 to send data packets while the connection which has reached the end boundary 202 of its Virtual Window 200 awaits additional acknowledgments.
- 10 [0033] If the TCP Next Data Pointer 22 has *not* reached the end boundary 202 of the Virtual Window 200, indicating that the stream of sent data has *not* reached (block 262) the end of the Virtual Window 200, the portion 256 is of nonzero size. Consequently, additional data packets can be sent (block 234) until the end of the Virtual Window is reached (block 262).
- [0034] It is recognized that the destination host may begin to run out of buffer space and as a result, advertise a smaller TCP Window 20. In which case, the value of the TCP Window stored in the field 21a, 21b ... 21n of the associated Protocol Control Block 222a, 222b ... 222n may be reset to this smaller value. If the TCP Window 20 of the destination host should become smaller (block 230) than the Virtual Window Maximum, the size of the Virtual Window 200 can be reset (block 270) to the size of the TCP Window as shown in FIG. 9.
- 15 20 Thus, the end boundary 202 of the virtual window 200 coincides with the end boundary 28 of the TCP Window 20. As a consequence, the Virtual Window 200 will not exceed the capabilities of the destination host as indicated by the TCP Window 200 advertised by the destination host.
- [0035] In the implementation of FIG. 7, the Virtual Window 200 is set to be the smaller of the TCP Window 20 and the Virtual Window Maximum. It is appreciated that the Virtual Window may be set using a variety of approaches. For example, the Virtual Window Maximum may be a fixed size such as 16 KB for example. Alternatively, the size of the Virtual
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Window may vary as a function of the size of the TCP Window advertised by the destination host. For example, the Virtual Window may be set as a fraction of the TCP Window. This fraction may be a fixed fraction, for example. Furthermore, the Virtual Window may be set as a function of the number of active connections of the driver or network adapter. Thus, for
5 example, the Virtual Window may be set as a fraction $1/N$ of the TCP Window, where N is the number of active connections of the driver or network adapter.

[0036] Still further, each Virtual Window Maximum 224a, 224b ... 224n may be programmed to allow a high quality of service (QoS) for one connection over another by providing a larger Virtual Window Maximum for those connections for which a higher QoS is
10 desired. Also, the Virtual Window Maximum may be changed at any time during a particular connection. Thus, the ability to arbitrarily change the Virtual Window Maximum associated with a particular connection can be used to change the QoS at any time in the life of the connection.

[0037] It is further appreciated that for some applications, one of the applications 114 (FIG.
15 4) of the computer 102 may generate an application queue 52a, 52b ... 52n (FIG. 3) for an RDMA connection 50a, 50b ... 50n which is substantially larger than the entire pool 64 of empty messages. This may occur for example if the application has a large number of messages to store in a specified memory location of a destination host over an RDMA connection. The RDMA connection having the large application queue can dominate usage of the empty
20 messages of the pool 64 and thereby continue to send messages in a manner which uses up the resources of the source host to the exclusion of other RDMA connections of the source host. As a consequence, the other RDMA connections of the source host may be hindered from sending messages.

[0038] In one implementation as shown in FIG. 10, the computer 102 when sending
25 messages over an RDMA connection 350a, 350b ... 350n imposes a limit on the number of empty messages of a pool 364 of a network interface 366, which any one of the RDMA connections 350a, 350b ... 350n can consume. More specifically, the pool 364 of empty

messages is subdivided into a plurality of Limited Pools of Empty Messages 372a, 372b ... 372n, each of which can be substantially smaller than the entire Pool of Empty Messages 364. When one of the RDMA connections 350a, 350b ... 350n has reached the limit imposed by its associated Limited Pool of Empty Messages 372a, 372b ... 372n, the host source 102 stops
5 pulling messages from the associated application queue 52a, 52b ... 52n of that particular RDMA connection even though other empty messages remain unused in the pool 364 of empty messages. The host source 102 resumes pulling messages from the associated application queue 52a, 52b ... 52n of that particular RDMA connection when additional empty messages are replenished in the associated Limited Pool of Empty Messages 372a, 372b ... 372n. As a
10 consequence, other RDMA connections are provided the opportunity to utilize the resources of the computer 102 such that the resources may be shared more fairly.

[0039] FIG. 11 illustrates operations of the device driver 120 and the network adapter 122 in using a Limited Pool of Empty Messages 372a, 372b ... 372n to distribute the message transmission resources of the computer 102. In this example, the RDMA connection 350a is
15 discussed. The other RDMA connections 350b ... 350n operate in a similar fashion.

[0040] In response to a request by a software application 114, the RDMA connection 350a is established (block 410) between the computer 102 and a destination host. In one implementation, the RDMA connection 350a runs over a TCP connection. Thus, in establishing the RDMA connection 350a, a Protocol Control Block 222a of the Protocol
20 Control Blocks 222a, 222b ... 222n (FIG. 8) is populated to include a field 17a for storing the TCP Unacknowledged Data Pointer 16, a field 21a for storing the TCP Window, and a field 23a for storing a TCP Next Pointer of the associated TCP connection in accordance with the TCP RFC. In addition, the TCP connection may utilize a Virtual Window 200 to provide fairness among the TCP connections of the computer 102 as discussed above. Hence, a
25 Virtual Window Maximum may be stored in a field 224a of the associated Protocol Control Block 222a.

[0041] The size of the Limited Pool of Empty Messages 372a of the particular RDMA connection 350a is set (block 420) to the size of the Message Limit value stored in a field 424a of the associated Protocol Control Block 222a. In the illustrated embodiment, the RDMA connection parameters are stored in the same Protocol Control Block as a TCP connection
5 parameters. It is appreciated that the RDMA connection parameters may be stored in a wholly separate Control Block or other data structure.

[0042] The computer 102 initiates the sending of messages to the specified address of the destination host by taking a message from the application queue 52a of the queue pair 51a of the particular RDMA connection 350a and queues (block 434) the message in the network
10 interface queue 60a which is paired with the associated application queue 52a of the software application. The queuing of messages in the network interface queue 60a consumes one empty message from the associated Limited Pool of Empty Messages 372a of the RDMA connection 350a for each message so queued. The number of messages queued in this step may vary depending upon the particular application. However, in many applications the amount
15 of data queued will be a relatively small proportion of the size of the Message Limit.

[0043] Following the queuing of this message or messages, a check (block 436) is made to determine if the destination host has acknowledged receiving any of the messages sent in the RDMA connection 350a. If so, an empty message is replenished (block 440) in the associated Limited Pool of Empty Messages 372a of the RDMA connection 350a for each message so
20 acknowledged.

[0044] A check is made (block 450) to determine whether the associated Limited Pool of Empty Messages 372a of the RDMA connection 350a is empty. If so, no more RDMA messages from the particular RDMA connection 350a may be queued in the network interface queue 60a for sending to the destination host until additional acknowledgments are received.
25 Hence, control is returned to block 436 to await further acknowledgments for RDMA connection 350a. If on the other hand, the Limited Pool of Empty Messages 372a of the RDMA connection 350a is not empty (block 450), additional RDMA messages from the

application queue 52a of the RDMA connection 350a may be queued in the network interface queue 60a for sending to the destination host. In this manner, the total number of messages queued in the network interface queue 60a plus the total number of uncompleted messages sent by the RDMA connection 350a may not exceed the size of the Message Limit used to set the
5 size of the Limited Pool of Empty Messages 372a of the RDMA connection 350a.

[0045] The RDMA connections 350b ... 350n operate in a similar manner. Hence, a Message Limit is stored in a field 424b ... 424n of the Protocol Control Block 222b ... 222n associated with each RDMA connection 350b ... 350n.

[0046] In the implementation of FIG. 11, the size of each Limited Pool of Empty Messages
10 372a, 372b ... 372n of the RDMA connections 350a, 350b ... 350n is set to be the Message Limit stored in the Protocol Control Block 222a, 222b ... 222n associated with each RDMA connection 350a, 350b ... 350n. It is appreciated that the size of each Limited Pool of Empty Messages 372a, 372b ... 372n may be set using a variety of approaches. For example, the Message Limit may be a fixed size, such as a fixed number of messages, for example. Also,
15 the size of the Message Limit for each RDMA connection may be set as a fixed fraction of the size of the Pool 364 of empty messages. Alternatively, the size of the Message Limit for each RDMA connection may vary as a function of the size of the associated application queue 52a, 52n ... 52n. For example, the size of the Message Limit for each RDMA connection may be set as a fraction of the size of the Pool 364 of empty messages wherein the size of each fraction
20 is proportionate to the size of the associated application queue 52a, 52n ... 52n. Furthermore, the Message Limit for each RDMA connection may be set as a function of the number of active connections of the driver or network adapter. For example, the size of the Message Limit for each RDMA connection may be set as a fraction $1/N$ of the size of the Pool 364 of empty messages where N is the number of active connections of the driver or network adapter. In this
25 example, the total of all of the Limited Pools of Empty Messages 372a, 372b ... 372n would remain equal to or less than the entire Pool 364 of empty messages. Also, each queue pair

51a, 51b ... 51n may have a separate pool of empty messages for each RDMA connection
350a, 350b ... 350n.

- [0047] Still further, each of the Limited Pools of Empty Messages 372a, 372b ... 372n may be programmed to allow a higher quality of service (QoS) for one connection over another by providing a larger Limited Pool of Empty Messages for those connections for which a higher QoS is desired. Also, the Limited Pool of Empty Messages may be changed at any time during a particular connection. Thus, the ability to arbitrarily change the Limited Pool of Empty Messages associated with a particular connection can be used to change the QoS at any time in the life of the connection.
- 10 [0048] Thus, it is seen that both the Virtual Window Maximum 224a, 224b ... 224n and the Limited Pool of Empty Messages 372a, 372b ... 372n may be programmed for each connection to allow different levels of quality of service (QoS) which can behave preferentially, depending upon the needs of the application. Moreover, both the Virtual Window Maximum 224a, 224b ... 224n the Limited Pool of Empty Messages 372a, 372b ... 372n may be
- 15 changed to change the QoS at any time in the life of the connection.

Additional Embodiment Details

- [0049] The described techniques for processing requests directed to a network card may be implemented as a method, apparatus or article of manufacture using standard programming and/or engineering techniques to produce software, firmware, hardware, or any combination thereof. The term "article of manufacture" as used herein refers to code or logic implemented in hardware logic (e.g., an integrated circuit chip, Programmable Gate Array (PGA), Application Specific Integrated Circuit (ASIC), etc.) or a computer readable medium, such as magnetic storage medium (e.g., hard disk drives, floppy disks,, tape, etc.), optical storage (CD-ROMs, optical disks, etc.), volatile and non-volatile memory devices (e.g., EEPROMs, ROMs, PROMs, RAMs, DRAMs, SRAMs, firmware, programmable logic, etc.). Code in the computer readable medium is accessed and executed by a processor. The code in which
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preferred embodiments are implemented may further be accessible through a transmission media or from a file server over a network. In such cases, the article of manufacture in which the code is implemented may comprise a transmission media, such as a network transmission line, wireless transmission media, signals propagating through space, radio waves, infrared
5 signals, etc. Thus, the “article of manufacture” may comprise the medium in which the code is embodied. Additionally, the “article of manufacture” may comprise a combination of hardware and software components in which the code is embodied, processed, and executed. Of course, those skilled in the art will recognize that many modifications may be made to this configuration without departing from the scope of the present invention, and that the article of
10 manufacture may comprise any information bearing medium known in the art.

[0050] In the described embodiments, certain operations were described as being performed by the device driver 120, or by protocol layers of the network adaptor 112. In alternative embodiments, operations described as performed by the device driver 120 may be performed by the network adaptor 112, and vice versa.

15 **[0051]** In the described embodiments, the packets are transmitted from a network adaptor card to a remote computer over a network. In alternative embodiments, the transmitted and received packets processed by the protocol layers or device driver may be transmitted to a separate process executing in the same computer in which the device driver and transport protocol driver execute. In such embodiments, the network card is not used as the packets are
20 passed between processes within the same computer and/or operating system.

[0052] In certain implementations, the device driver and network adaptor embodiments may be included in a computer system including a storage controller, such as a SCSI, Integrated Drive Electronics (IDE), Redundant Array of Independent Disk (RAID), etc., controller, that manages access to a non-volatile storage device, such as a magnetic disk drive, tape media,
25 optical disk, etc. In alternative implementations, the network adaptor embodiments may be included in a system that does not include a storage controller, such as certain hubs and switches.

[0053] In certain implementations, the device driver and network adaptor embodiments may be implemented in a computer system including a video controller to render information to display on a monitor coupled to the computer system including the device driver and network adaptor, such as a computer system comprising a desktop, workstation, server, mainframe, 5 laptop, handheld computer, etc. Alternatively, the network adaptor and device driver embodiments may be implemented in a computing device that does not include a video controller, such as a switch, router, etc.

[0054] In certain implementations, the network adaptor may be configured to transmit data across a cable connected to a port on the network adaptor. Alternatively, the network adaptor 10 embodiments may be configured to transmit data over a wireless network or connection, such as wireless LAN, Bluetooth, etc.

[0055] FIG. 8 illustrates information used to populate Protocol Control Blocks. In alternative implementation, these data structures may include additional or different information than illustrated in the figures.

15 [0056] The illustrated logic of FIGs. 7 and 11 show certain events occurring in a certain order. In alternative embodiments, certain operations may be performed in a different order, modified or removed. Moreover, steps may be added to the above described logic and still conform to the described embodiments. Further, operations described herein may occur sequentially or certain operations may be processed in parallel. Yet further, operations may be 20 performed by a single processing unit or by distributed processing units.

[0057] FIG. 12 illustrates one implementation of a computer architecture 500 of the network components, such as the hosts and storage devices shown in FIGs. 4 and 10. The architecture 500 may include a processor 502 (e.g., a microprocessor), a memory 504 (e.g., a volatile memory device), and storage 506 (e.g., a non-volatile storage, such as magnetic disk drives, 25 optical disk drives, a tape drive, etc.). The storage 506 may comprise an internal storage device or an attached or network accessible storage. Programs in the storage 506 are loaded into the memory 504 and executed by the processor 502 in a manner known in the art. The

architecture further includes a network card 508 to enable communication with a network, such as an Ethernet, a Fibre Channel Arbitrated Loop, etc. Further, the architecture may, in certain embodiments, include a video controller 509 to render information on a display monitor, where the video controller 509 may be implemented on a video card or integrated on integrated circuit components mounted on the motherboard. As discussed, certain of the network devices may have multiple network cards. An input device 510 is used to provide user input to the processor 502, and may include a keyboard, mouse, pen-stylus, microphone, touch sensitive display screen, or any other activation or input mechanism known in the art. An output device 512 is capable of rendering information transmitted from the processor 502, or other component, such as a display monitor, printer, storage, etc.

[0058] The network adaptor 508 may be implemented on a network card, such as a Peripheral Component Interconnect (PCI) card or some other I/O card, or on integrated circuit components mounted on the motherboard or in software.

[0059] The foregoing description of various embodiments of the invention has been presented for the purposes of illustration and description. It is not intended to be exhaustive or to limit the invention to the precise form disclosed. Many modifications and variations are possible in light of the above teaching. It is intended that the scope of the invention be limited not by this detailed description, but rather by the claims appended hereto. The above specification, examples and data provide a complete description of the manufacture and use of the composition of the invention. Since many embodiments of the invention can be made without departing from the spirit and scope of the invention, the invention resides in the claims hereinafter appended.